

Decision Procedures

An Algorithmic Point of View

Decision Procedures for Propositional Logic

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Part I

Decision Procedures for Propositional Logic

Outline

- 1 Modeling with Propositional Logic
 - SAT Example: Equivalence Checking if-then-else Chains
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SAT Example: Equivalence Checking if-then-else Chains

Optimization of if-then-else chains

original C code		optimized C code
<pre>if(!a && !b) h(); else if(!a) g(); else f();</pre>		<pre>if(a) f(); else if(b) g(); else h();</pre>
↓		↑
<pre>if(!a) { if(!b) h(); else g(); } else f();</pre>	⇒	<pre>if(a) f(); else { if(!b) h(); else g(); }</pre>

SAT Example II

- 1 Represent procedures as *independent* Boolean variables

$original :=$ <pre>if $\neg a \wedge \neg b$ then h else if $\neg a$ then g else f</pre>	$optimized :=$ <pre>if a then f else if b then g else h</pre>
--	---

- 2 Compile if-then-else chains into Boolean formulae

$$\text{compile}(\text{if } x \text{ then } y \text{ else } z) \equiv (x \wedge y) \vee (\neg x \wedge z)$$

- 3 Check equivalence of Boolean formulae

$$\text{compile}(original) \Leftrightarrow \text{compile}(optimized)$$

"Compilation"

$$\begin{aligned}
 original &\equiv \text{if } \neg a \wedge \neg b \text{ then } h \text{ else if } \neg a \text{ then } g \text{ else } h \\
 &\equiv (\neg a \wedge \neg b) \wedge h \vee \neg(\neg a \wedge \neg b) \wedge \text{if } \neg a \text{ then } g \text{ else } h \\
 &\equiv (\neg a \wedge \neg b) \wedge h \vee \neg(\neg a \wedge \neg b) \wedge (\neg a \wedge g \vee a \wedge f)
 \end{aligned}$$

$$\begin{aligned}
 optimized &\equiv \text{if } a \text{ then } f \text{ else if } b \text{ then } g \text{ else } h \\
 &\equiv a \wedge f \vee \neg a \wedge \text{if } b \text{ then } g \text{ else } h \\
 &\equiv a \wedge f \vee \neg a \wedge (b \wedge g \vee \neg b \wedge h)
 \end{aligned}$$

$$(\neg a \wedge \neg b) \wedge h \vee \neg(\neg a \wedge \neg b) \wedge (\neg a \wedge g \vee a \wedge f) \Leftrightarrow a \wedge f \vee \neg a \wedge (b \wedge g \vee \neg b \wedge h)$$

How to Check (In)Equivalence?

Reformulate it as a satisfiability (SAT) problem:

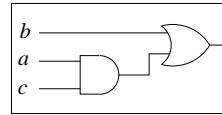
Is there an assignment to a, b, f, g, h , which results in different evaluations of *original* and *optimized*?

or equivalently:

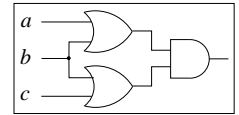
Is the boolean formula $\text{compile}(\text{original}) \neq \text{compile}(\text{optimized})$ satisfiable?

Such an assignment provides an easy to understand counterexample

SAT Example: Circuit Equivalence Checking



$$b \vee a \wedge c$$



$$(a \vee b) \wedge (b \vee c)$$

equivalent?

$$b \vee a \wedge c$$

\Leftrightarrow

$$(a \vee b) \wedge (b \vee c)$$

SAT

SAT (Satisfiability) the classical NP-complete problem:
Given a propositional formula f over n propositional variables $V = \{x, y, \dots\}$.

Is there an assignment $\sigma : V \rightarrow \{0, 1\}$ with $\sigma(f) = 1$?

SAT

- **SAT belongs to NP**

There is a *non-deterministic* Turing-machine deciding SAT in polynomial time:

guess the assignment σ (linear in n), calculate $\sigma(f)$ (linear in $|f|$)

Note: on a *real* (deterministic) computer this still requires 2^n time

- **SAT is complete for NP** (see complexity / theory class)

Implications for us: general SAT algorithms are **probably exponential in time** (unless $NP = P$)

Conjunctive Normal Form

Definition (Conjunctive Normal Form)

A formula in *Conjunctive Normal Form* (CNF) is a conjunction of clauses

$$C_1 \wedge C_2 \wedge \dots \wedge C_n$$

each clause C is a disjunction of literals

$$C = L_1 \vee \dots \vee L_m$$

and each literal is either a plain variable x or a negated variable \bar{x} .

Example $(a \vee b \vee c) \wedge (\bar{a} \vee \bar{b}) \wedge (\bar{a} \vee \bar{c})$

CNF for Parity Function is Exponential

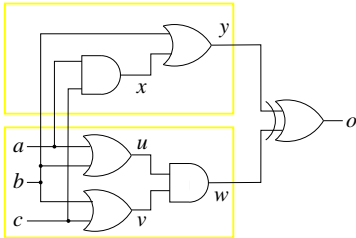
	b			
	0	1	0	1
a	1	0	1	0
	0	1	0	1
	1	0	1	0
	1	0	1	0
	d			

$$a \oplus b \oplus c \oplus d$$

- no merging in the Karnaugh map
- all clauses contain all variables
- CNF for parity with n variables has 2^{n-1} clauses

Better ideas?

Example of Tseitin Transformation: Circuit to CNF



$$\begin{aligned}
 & o \wedge \\
 & (x \leftrightarrow a \wedge b) \wedge \\
 & (y \leftrightarrow b \vee x) \wedge \\
 & (u \leftrightarrow a \vee b) \wedge \\
 & (v \leftrightarrow b \vee c) \wedge \\
 & (w \leftrightarrow u \wedge v) \wedge \\
 & (o \leftrightarrow y \oplus w)
 \end{aligned}$$

$$\begin{aligned}
 & o \wedge (x \rightarrow a) \wedge (x \rightarrow c) \wedge (x \leftarrow a \wedge c) \wedge \dots \\
 & o \wedge (\bar{x} \vee a) \wedge (\bar{x} \vee c) \wedge (x \vee \bar{a} \vee \bar{c}) \wedge \dots
 \end{aligned}$$

Algorithmic Description of Tseitin Transformation

Tseitin Transformation

- 1 For each non input circuit signal s generate a new variable x_s
 - 2 For each gate produce complete input / output constraints as clauses
 - 3 Collect all constraints in a big conjunction
- The transformation is **satisfiability equivalent**: the result is satisfiable iff and only the original formula is satisfiable
 - **Not equivalent** in the classical sense to original formula: it has new variables
 - You can get a satisfying assignment for original formula by projecting the satisfying assignment onto the original variables

Tseitin Transformation: Input / Output Constraints

Negation: $x \leftrightarrow \bar{y} \Leftrightarrow (x \rightarrow \bar{y}) \wedge (\bar{y} \rightarrow x)$
 $\Leftrightarrow (\bar{x} \vee \bar{y}) \wedge (y \vee x)$

Disjunction: $x \leftrightarrow (y \vee z) \Leftrightarrow (y \rightarrow x) \wedge (z \rightarrow x) \wedge (x \rightarrow (y \vee z))$
 $\Leftrightarrow (\bar{y} \vee x) \wedge (\bar{z} \vee x) \wedge (x \vee y \vee z)$

Conjunction: $x \leftrightarrow (y \wedge z) \Leftrightarrow (x \rightarrow y) \wedge (x \rightarrow z) \wedge ((y \wedge z) \rightarrow x)$
 $\Leftrightarrow (\bar{x} \vee y) \wedge (\bar{x} \vee z) \wedge ((y \wedge z) \vee x)$
 $\Leftrightarrow (\bar{x} \vee y) \wedge (\bar{x} \vee z) \wedge (\bar{y} \vee \bar{z} \vee x)$

Equivalence: $x \leftrightarrow (y \leftrightarrow z) \Leftrightarrow (x \rightarrow (y \leftrightarrow z)) \wedge ((y \leftrightarrow z) \rightarrow x)$
 $\Leftrightarrow (x \rightarrow ((y \rightarrow z) \wedge (z \rightarrow y))) \wedge ((y \leftrightarrow z) \rightarrow x)$
 $\Leftrightarrow (x \rightarrow (y \rightarrow z)) \wedge (x \rightarrow (z \rightarrow y)) \wedge ((y \leftrightarrow z) \rightarrow x)$
 $\Leftrightarrow (\bar{x} \vee \bar{y} \vee z) \wedge (\bar{x} \vee \bar{z} \vee y) \wedge ((y \leftrightarrow z) \rightarrow x)$
 $\Leftrightarrow (\bar{x} \vee \bar{y} \vee z) \wedge (\bar{x} \vee \bar{z} \vee y) \wedge (((y \wedge z) \vee (\bar{y} \wedge \bar{z})) \rightarrow x)$
 $\Leftrightarrow (\bar{x} \vee \bar{y} \vee z) \wedge (\bar{x} \vee \bar{z} \vee y) \wedge ((y \wedge z) \rightarrow x) \wedge ((\bar{y} \wedge \bar{z}) \rightarrow x)$
 $\Leftrightarrow (\bar{x} \vee \bar{y} \vee z) \wedge (\bar{x} \vee \bar{z} \vee y) \wedge (\bar{y} \vee \bar{z} \vee x) \wedge (y \vee z \vee x)$

Optimizations for the Tseitin Transformation

- Goal is smaller CNF (less variables, less clauses)
- Extract multi argument operands (removes variables for intermediate nodes)
- NNF: half of AND, OR node constraints may be removed due to monotonicity
- use *sharing*

DIMACS CNF

- DIMACS CNF format = standard format for CNF
- Used by most SAT solvers
- Plain text file with following structure:


```
p cnf <# variables> <# clauses>
<clause> 0
<clause> 0
...
```
- One or more lines per clause

DIMACS CNF

- Every clause is a list of numbers, separated by spaces
- A clause ends with 0
- Every number 1, 2, ... corresponds to a variable
 - variable names (e.g., a, b, \dots) have to be mapped to numbers
- A negative number corresponds to negation
 - Let a have number 5. Then \bar{a} is -5.

DIMACS CNF: Example

$$(x_1 \vee x_2 \vee \bar{x}_3) \wedge (x_2 \vee \bar{x}_1) \wedge (x_4 \vee \bar{x}_2 \vee \bar{x}_1)$$

- 4 variables, 3 clauses
- CNF file:

```
p cnf 4 3
1 2 -3 0
2 -1 0
4 -2 -1 0
```

Example SAT: Circuit Equivalence

formula:

$$\begin{aligned}
 & o \wedge \\
 & (x \leftrightarrow a \wedge c) \wedge \\
 & (y \leftrightarrow b \vee x) \wedge \\
 & (u \leftrightarrow a \vee b) \wedge \\
 & (v \leftrightarrow b \vee c) \wedge \\
 & (w \leftrightarrow u \wedge v) \wedge \\
 & (o \leftrightarrow y \oplus w)
 \end{aligned}$$

number assignment:

variable	number
<i>o</i>	1
<i>a</i>	2
<i>c</i>	3
<i>x</i>	4
<i>b</i>	5
<i>y</i>	6
<i>u</i>	7
<i>v</i>	8
<i>w</i>	9

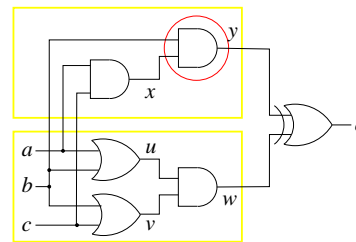
Simply in order of occurrence.

Example SAT: Circuit Equivalence

formula	clauses	DIMACS
<i>o</i>	<i>o</i>	1 0
$x \leftrightarrow a \wedge c$	$a \vee \bar{x}$ $c \vee \bar{x}$	2 -4 0 3 -4 0
$y \leftrightarrow b \vee x$	$\bar{a} \vee \bar{c} \vee x$ $\bar{x} \vee y$	-2 -3 4 0 -4 6 0
$u \leftrightarrow a \vee b$	$\bar{b} \vee y$ $x \vee \bar{b} \vee \bar{y}$	-5 6 0 4 5 -6 0
$v \leftrightarrow b \vee c$	$\bar{a} \vee u$ $\bar{b} \vee u$	-2 7 0 -5 7 0
$w \leftrightarrow u \wedge v$	$a \vee \bar{b} \vee \bar{u}$ $\bar{b} \vee v$	2 5 -7 0 -5 8 0
$o \leftrightarrow y \oplus w$	$\bar{c} \vee v$ $b \vee c \vee \bar{v}$	-3 8 0 5 3 -8 0
	$u \vee \bar{w}$ $v \vee \bar{w}$	7 -9 0 8 -9 0
	$\bar{u} \vee \bar{v} \vee w$	-7 -8 9 0
	$\bar{y} \vee \bar{w} \vee \bar{o}$ $y \vee w \vee \bar{o}$ $\bar{y} \vee w \vee o$	-6 -9 -1 0 6 9 -1 0 -6 9 1 0

Example SAT: Circuit Equivalence

Let's change the circuit!



$$\begin{aligned}
 & o \wedge \\
 & (x \leftrightarrow a \wedge c) \wedge \\
 & (y \leftrightarrow b \wedge x) \wedge \\
 & (u \leftrightarrow a \vee b) \wedge \\
 & (v \leftrightarrow b \vee c) \wedge \\
 & (w \leftrightarrow u \wedge v) \wedge \\
 & (o \leftrightarrow y \oplus w)
 \end{aligned}$$

Is the CNF satisfiable?

Example SAT: Circuit Equivalence

- Output of the SAT solver:

SATISFIABLE
1 2 3 4 -5 -6 7 8 9

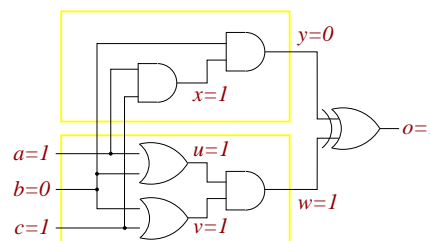
- Values of the variables:

variable	number	value
<i>o</i>	1	1
<i>a</i>	2	1
<i>c</i>	3	1
<i>x</i>	4	1
<i>b</i>	5	0
<i>y</i>	6	0
<i>u</i>	7	1
<i>v</i>	8	1
<i>w</i>	9	1

- Caveat: there is more than one solution

Example SAT: Circuit Equivalence

Satisfying assignment mapped to the circuit:



variable	value
<i>o</i>	1
<i>a</i>	1
<i>c</i>	1
<i>x</i>	1
<i>b</i>	0
<i>y</i>	0
<i>u</i>	1
<i>v</i>	1
<i>w</i>	1